# Linyu Wu

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### **EDUCATION**

#### Shanghai Jiao Tong University

Shanghai, China Sep. 2021 - Present

Bachelor in Computer Science

• A member of <u>ACM Honored Class</u>, an elite CS program for the top 5% talented students.

### RESEARCH INTERESTS

I'm interested in building efficient, reliable, and scalable computer systems.

Currently working on federated query processing for heterogeneous RDBMSes.

### **Experience**

### Analytical DataBase Lab

University at Buffalo Sep. 2023 - Present

Research Intern

• Work on federated query processing, advised by **Prof. Zhuoyue Zhao** and **Prof. Xie Dong**.

Apex Lab

Shanghai Jiao Tong University Aug. 2023 - Present

Research Intern

• Work on LLM deployment optimization with TVM, advised by **Dr. Siyuan Feng** .

### Personal Projects

### File Synchronizer

Celve/rfsync

Async Rust

Jul. 2023 - Aug. 2023

• A real-time file synchronizer based on async Rust.

### **OS Kernel**

Celve/recore

Rust

Feb. 2023 - Jul. 2023

• A toy Rust-written OS kernel written with 10k+ lines of code, which supports SMP, virtual memory, file system, virtio, and more.

### Compiler

Celve/meteor

Kotlin

Sep. 2022 - Jan. 2023

• A compiler for Mx\*, a C-and-Java-like programming language with 15k+ lines of codes, with performance surpass LLVM O2 in most test cases.

#### **RISC-V CPU**

Celve/resin

Verilog

Oct. 2022 - Nov. 2022

 $\bullet\,$  A RISC-V CPU that supports RV32I Base Integer instruction Set V2.0 (2.1-2.7), based on Tomasulo and written in Verilog.

Go Jul. 2022 - Aug. 2022

• A shard kv database based on extended Raft paper.

## **SKILLS**

- **Programming Language**: **multilingual**, especially experienced in Rust, comfortable with C++, Kotlin, Go, Python, and Java.
- Database: experienced with Calcite framework, familiar with Postgres, MariaDB, and SQL.
- Mlsys: familiar with TVM and MLC-LLM.